

Features

- Fast Read Access Time - 45 ns
- Low-Power CMOS Operation
 - 100 μ A max. Standby
 - 20 mA max. Active at 5 MHz
- JEDEC Standard Packages
 - 28-Lead 600-mil PDIP
 - 32-Lead PLCC
 - 28-Lead TSOP and SOIC
- 5V \pm 10% Supply
- High Reliability CMOS Technology
 - 2,000V ESD Protection
 - 200 mA Latchup Immunity
- Rapid™ Programming Algorithm - 100 μ s/byte (typical)
- CMOS and TTL Compatible Inputs and Outputs
- Integrated Product Identification Code
- Commercial, Industrial and Automotive Temperature Ranges

Description

The AT27C256R is a low-power, high-performance 262,144-bit one-time programmable read only memory (OTP EPROM) organized 32K by 8 bits. It requires only one 5V power supply in normal read mode operation. Any byte can be accessed in less than 45 ns, eliminating the need for speed reducing WAIT states on high-performance microprocessor systems.

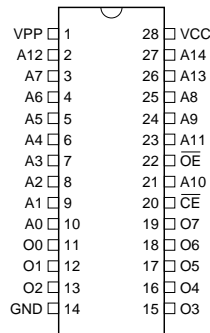
Atmel's scaled CMOS technology provides low-active power consumption, and fast programming. Power consumption is typically only 8 mA in Active Mode and less than 10 μ A in Standby.

(continued)

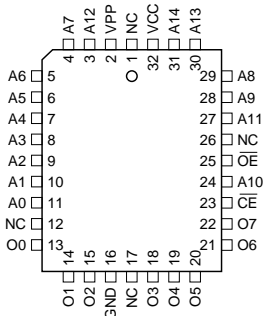
Pin Configurations

Pin Name	Function
A0 to A14	Addresses
O0 - O7	Outputs
$\overline{\text{CE}}$	Chip Enable
$\overline{\text{OE}}$	Output Enable
NC	No Connect

PDIP, SOIC Top View

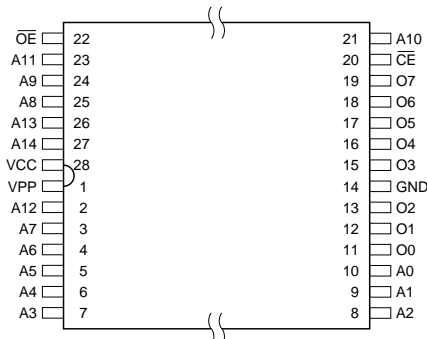


PLCC Top View



TSOP Top View

Type 1



Note: PLCC Package Pins 1 and 17 are DON'T CONNECT.



256K (32K x 8)
OTP EPROM

AT27C256R

