

CAT28C16A

16K-Bit CMOS E²PROM

FEATURES

- Fast Read Access Times: 200 ns
- Low Power CMOS Dissipation:
 - Active: 25 mA Max.
 - Standby: 100 μ A Max.
- Simple Write Operation:
 - On-Chip Address and Data Latches
 - Self-Timed Write Cycle with Auto-Clear
- Fast Write Cycle Time: 10ms Max
- End of Write Detection: $\overline{\text{DATA}}$ Polling
- Hardware Write Protection
- CMOS and TTL Compatible I/O
- 10,000 Program/Erase Cycles
- 10 Year Data Retention
- Commercial and Industrial Temperature Ranges

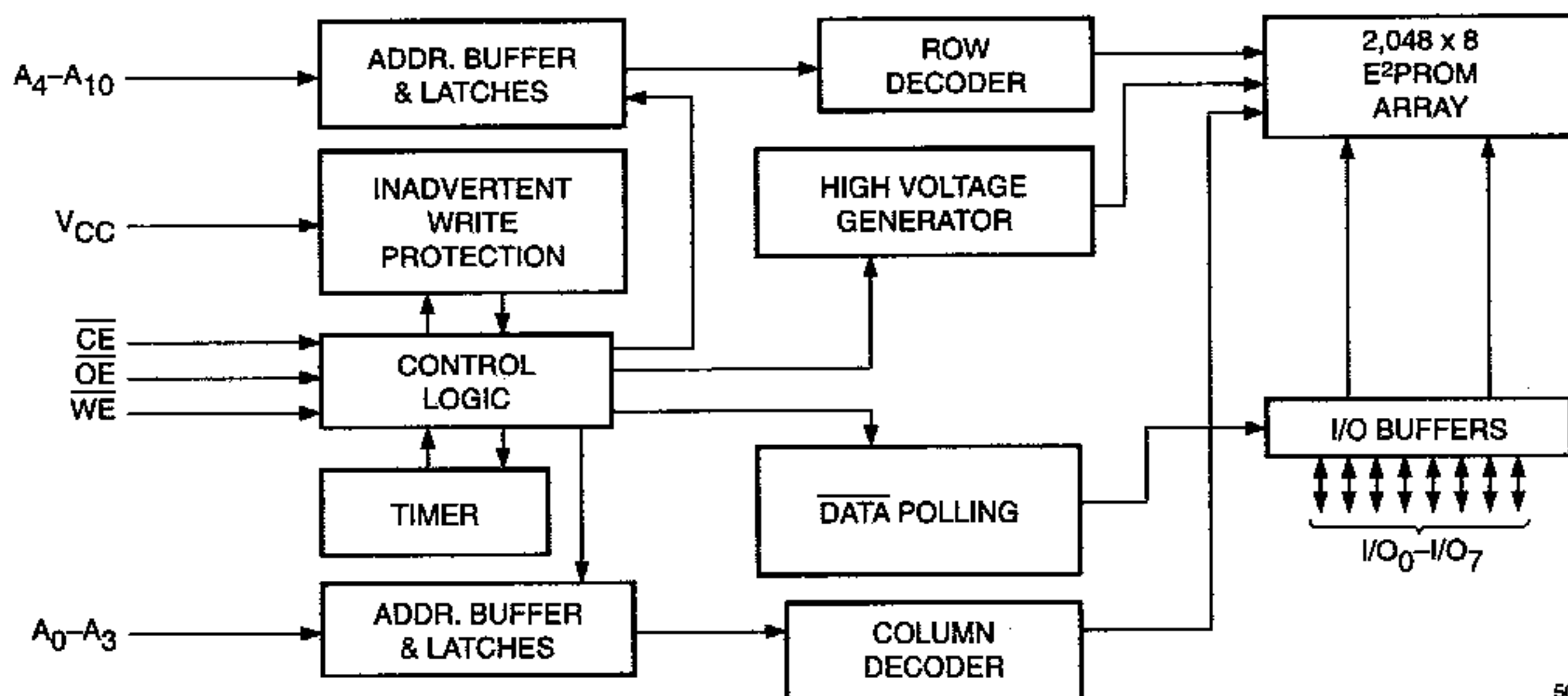
DESCRIPTION

The CAT28C16A is a fast, low power, 5V-only CMOS E²PROM organized as 2K x 8-bits. It requires a simple interface for in-system programming. On-chip address and data latches, self-timed write cycle with auto-clear and V_{CC} power up/down write protection eliminate additional timing and protection hardware. $\overline{\text{DATA}}$ Polling signals the start and end of the self-timed write cycle. Additionally, the CAT28C16A features hardware write protection.

The CAT28C16A is manufactured using Catalyst's advanced CMOS floating gate technology. It is designed to endure 10,000 program/erase cycles and has a data retention of 10 years. The device is available in JEDEC approved 24-pin DIP and SOIC or 32-pin PLCC packages.

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BLOCK DIAGRAM



5089 FHD F02