

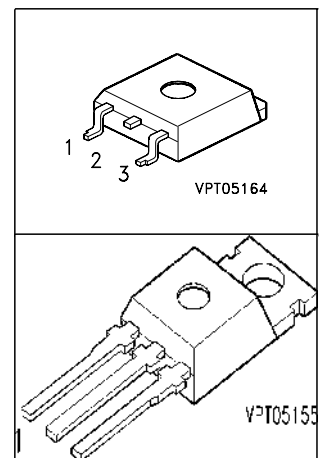
# Smart Lowside Power Switch

## Features

- Logic Level Input
- Input Protection (ESD)
- Thermal shutdown with latch
- Overload protection
- Short circuit protection
- Overvoltage protection
- Current limitation
- Status feedback with external input resistor
- Analog driving possible

## Product Summary

Drain source voltage	$V_{DS}$	60	V
On-state resistance	$R_{DS(on)}$	18	m $\Omega$
Current limit	$I_{D(lim)}$	30	A
Nominal load current	$I_{D(ISO)}$	19	A
Clamping energy	$E_{AS}$	6000	mJ



## Application

- All kinds of resistive, inductive and capacitive loads in switching or linear applications
- $\mu$ C compatible power switch for 12 V and 24 V DC applications
- Replaces electromechanical relays and discrete circuits

## General Description

N channel vertical power FET in Smart SIPMOS® chip on chip technology. Fully protected by embedded protected functions.

